

S.N.: 09/995,095  
Art Unit: 2638

### REMARKS

Claims 1-25 are currently pending. Claims 5 and 11 have been amended for clarification purposes only. Claims 22-25 have been added to further define applicant's claimed invention. The support for the limitations of claims 22-25 is found on page 6, lines 27-30, and on page 8, lines 1-3, and elsewhere of the specification as filed. It is respectfully submitted that no new matter has been added.

The specification has been amended to incorporate the subject matter of original claims 8 and 11 into the specification as related to original Figure 2. It is respectfully submitted that no new matter has been added.

The Patent Office rejected claims 8 and 17 under 35 U.S.C. 102(b) as being anticipated by Zhou et al., U.S. Patent No. 6,064,690.

For a claim to be anticipated, each and every non-inherent limitation of that claim must be disclosed by a reference. (See, for example, MPEP § 2131)

Claim 8 recites "A device for frame sync detection using channel combining and correlation, the device comprising: a channel despreader, wherein the channel despreader provides at least two each in-phase I1-In and, quadrature phase Q1-Qn channels, where  $n \geq 2$ ; at least one I-sync processor, wherein the I-sync processor is coupled to the channel despreader; at least one Q-sync processor, wherein the Q-sync processor is coupled to the channel despreader; an address controller coupled to the I-sync processor and the Q-sync processor; **a first summer connected to the I-sync processor and the Q-sync processor**; and a comparator, wherein the comparator is coupled to the first summer."

Claim 17 recites "An integrated circuit (IC), wherein the integrated circuit comprises: a channel despreader, wherein the channel despreader provides at least two each in-phase I1-In and, quadrature phase Q1-Qn channels, where  $n \geq 2$ ; at least one I-sync processor, wherein the I-sync processor is coupled to the channel despreader; at least one Q-sync processor, wherein the Q-sync processor is coupled to the channel despreader; an address controller coupled to the I-sync processor and the Q-sync processor; **a first summer connected to the I-sync processor and the Q-sync processor**; and a comparator, wherein the comparator is coupled to the first summer."

Zhou is quite different from the claimed invention as Zhou is concerned with phase synchronization whereas Applicant's invention is directed to frame synchronization. Zhou does not disclose that a first summer connected to the I-sync processor and the Q-sync processor. The Patent Office asserted that the rake combiner 60 connects the provisional judgment 10 and the phase correction 20 (see Figure 7). Zhou discloses provisional judgment portion 10 that provides an input to the phase correction portion 20, but does not appear to disclose or show that the provisional judgment portion 10 provides a direct input (i.e., a connection) to the rake combiner 60. Furthermore, Zhou does not appear to disclose that the phase correction portion is either an I-sync processor or a Q-sync processor; likewise, Zhou does not appear to disclose that the provisional judgment portion is either an I-sync processor or a Q-sync processor. Zhou does not sum separate I and Q signals. Zhou discloses (col. 7, lines 10-18; col. 8, lines 30-55) combined I and Q signals. What the Patent Office refers to as a summer of I and Q signals is a rake combiner for phase corrected synchronized signals, each of which is already a combination of I and Q signals.

Thus, claims 8-10 and 17-19 are not anticipated by Zhou.

The Patent Office rejected claim 10 under 35 U.S.C. 103(a) as being unpatentable over Zhou.

The Patent Office rejected claims 18 and 19 under 35 U.S.C. 103(a) as being unpatentable over Zhou as applied to claim 17 in view of Rhodes et al., U.S. Patent No. 5,909,437.

Claims 9, 10, 18, and 19 are allowable because they depend from an allowable base claim – either from claim 1 or from claim 17.

Newly added claims 22-25 are believed to be allowable over the prior art of record.

The Patent Office is thanked for the indication that claims 1-7, 20, and 21 are allowed and that claims 11-16 contain allowable subject matter. Applicant believes all pending claims 1-25 are allowable.

The Patent Office is respectfully requested to reconsider and remove the rejections of the claims 1-21 under 35 U.S.C. 102(b) based on Zhou or under 35 U.S.C. 103(a) based on Zhou, alone or in combination with Rhodes, and to allow all of the pending claims 1-25 as now presented for examination. An early notification of the allowability of claims 1-25 is earnestly solicited.

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